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Fig.6A
(RELATED ART)

INTERSYMBOL
INTERFERENCE
ELIMINATION
PREPARATION
AND AUTO-ZERO
OPERATIONS

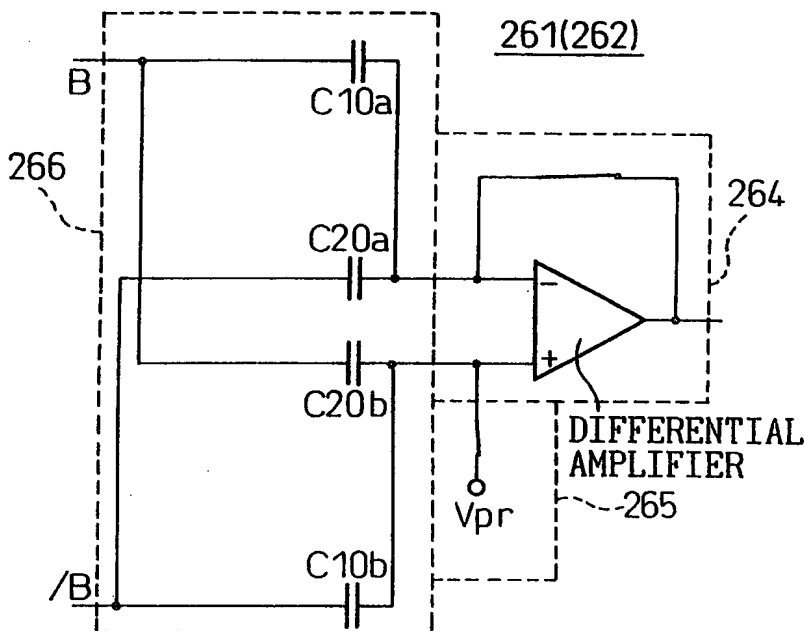
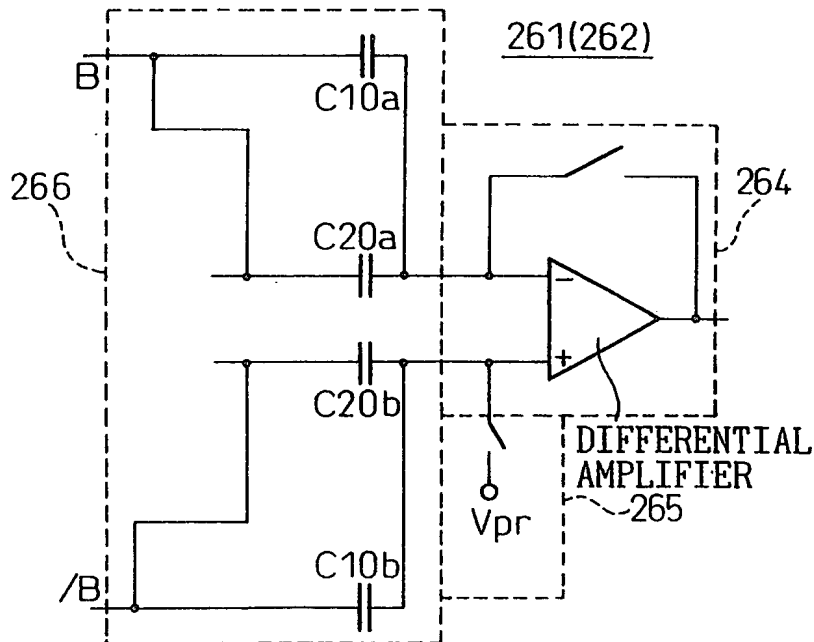
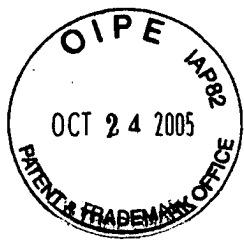


Fig.6B
(RELATED ART)

SIGNAL
DECISION
OPERATION





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Fig.7A

(PRIOR ART)

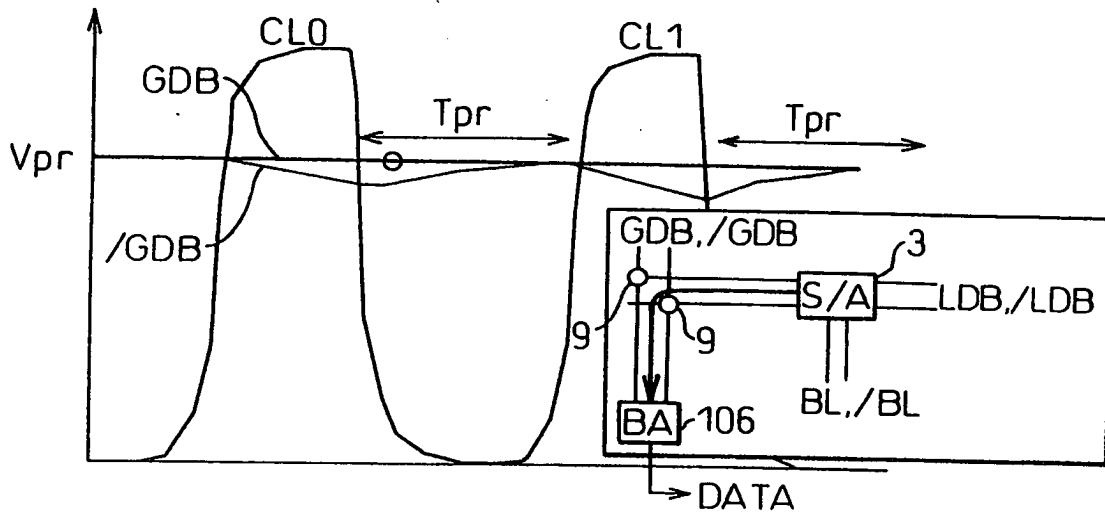
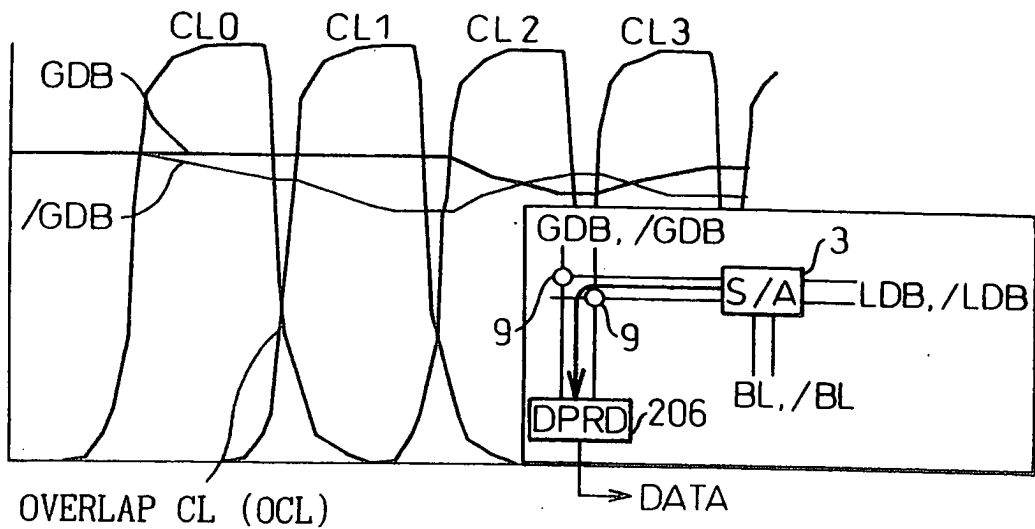


Fig.7B

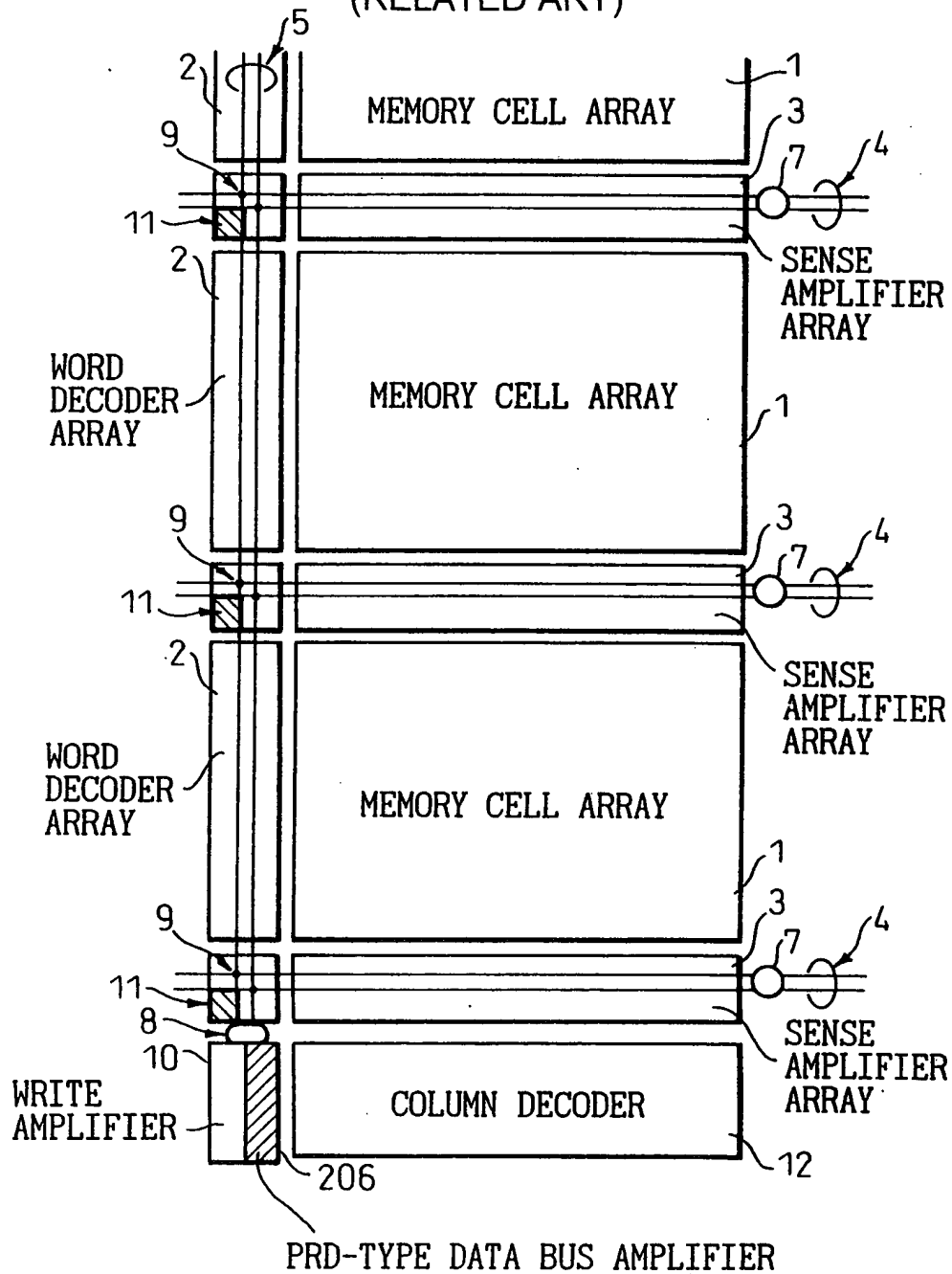
(RELATED ART)

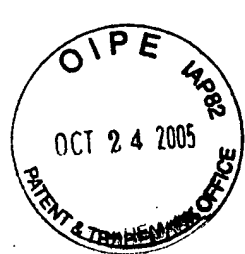




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Fig.8
(RELATED ART)



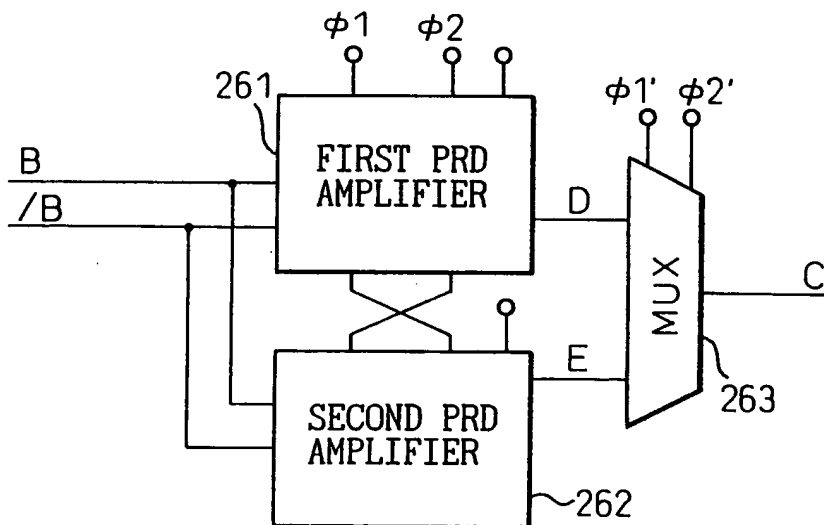


Title: SIGNAL TRANSMISSION SYSTEM USING PRD METHOD,
RECEIVER CIRCUIT FOR USE IN THE SIGNAL TRANSMISSION
SYSTEM, AND SEMICONDUCTOR MEMORY DEVICE TO WHICH
THE SIGNAL TRANSMISSION SYSTEM IS APPLIED
Inventor: KUMAGAI et al.
Application No. 09/714,285
Docket No. 100021-00033

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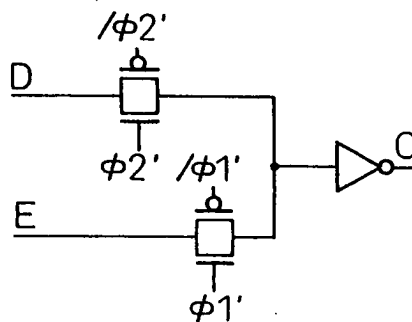
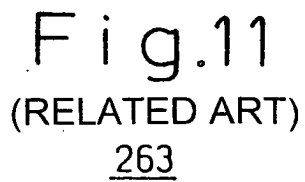
Fig.9
(RELATED ART)

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$$\frac{9}{66}$$

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Title: SIGNAL TRANSMISSION SYSTEM USING PRD METHOD,
RECEIVER CIRCUIT FOR USE IN THE SIGNAL TRANSMISSION SYSTEM,
AND SEMICONDUCTOR MEMORY DEVICE TO WHICH THE SIGNAL
TRANSMISSION SYSTEM IS APPLIED
Inventor: KUMAGAI et al.
Application No. 09/714,285
Docket No. 100021-00033

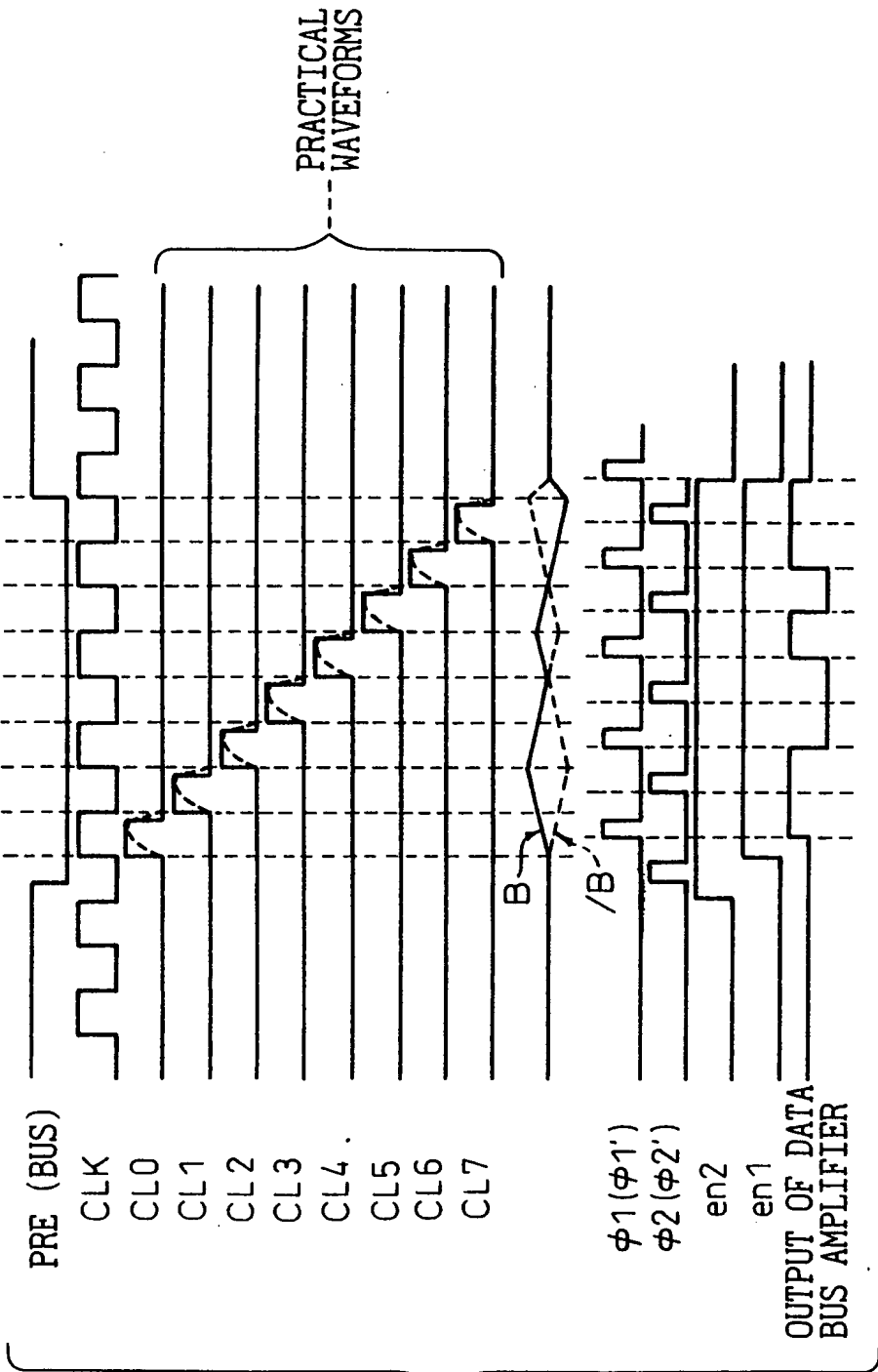


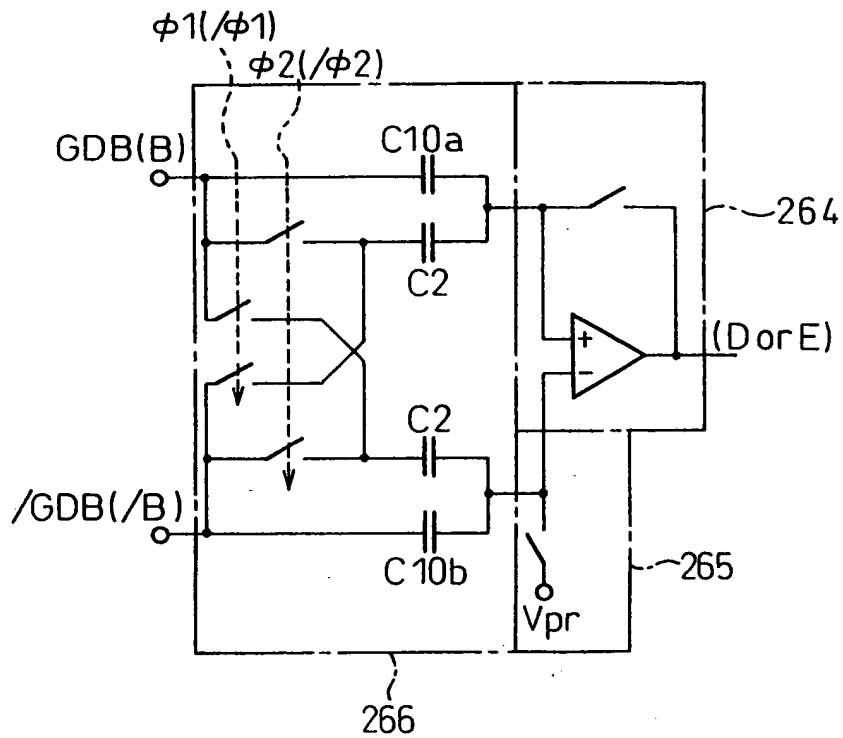
Fig.12
(RELATED ART)



Title: SIGNAL TRANSMISSION SYSTEM USING PRD METHOD,
RECEIVER CIRCUIT FOR USE IN THE SIGNAL TRANSMISSION SYSTEM,
AND SEMICONDUCTOR MEMORY DEVICE TO WHICH THE SIGNAL
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Inventor: KUMAGAI et al.
Application No. 09/714,285
Docket No. 100021-00033

Fig.15
(RELATED ART)

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Title: SIGNAL TRANSMISSION SYSTEM USING PRD METHOD,
RECEIVER CIRCUIT FOR USE IN THE SIGNAL TRANSMISSION SYSTEM,
AND SEMICONDUCTOR MEMORY DEVICE TO WHICH THE SIGNAL
TRANSMISSION SYSTEM IS APPLIED
Inventor: KUMAGAI et al.
Application No. 09/714,285
Docket No. 100021-00033

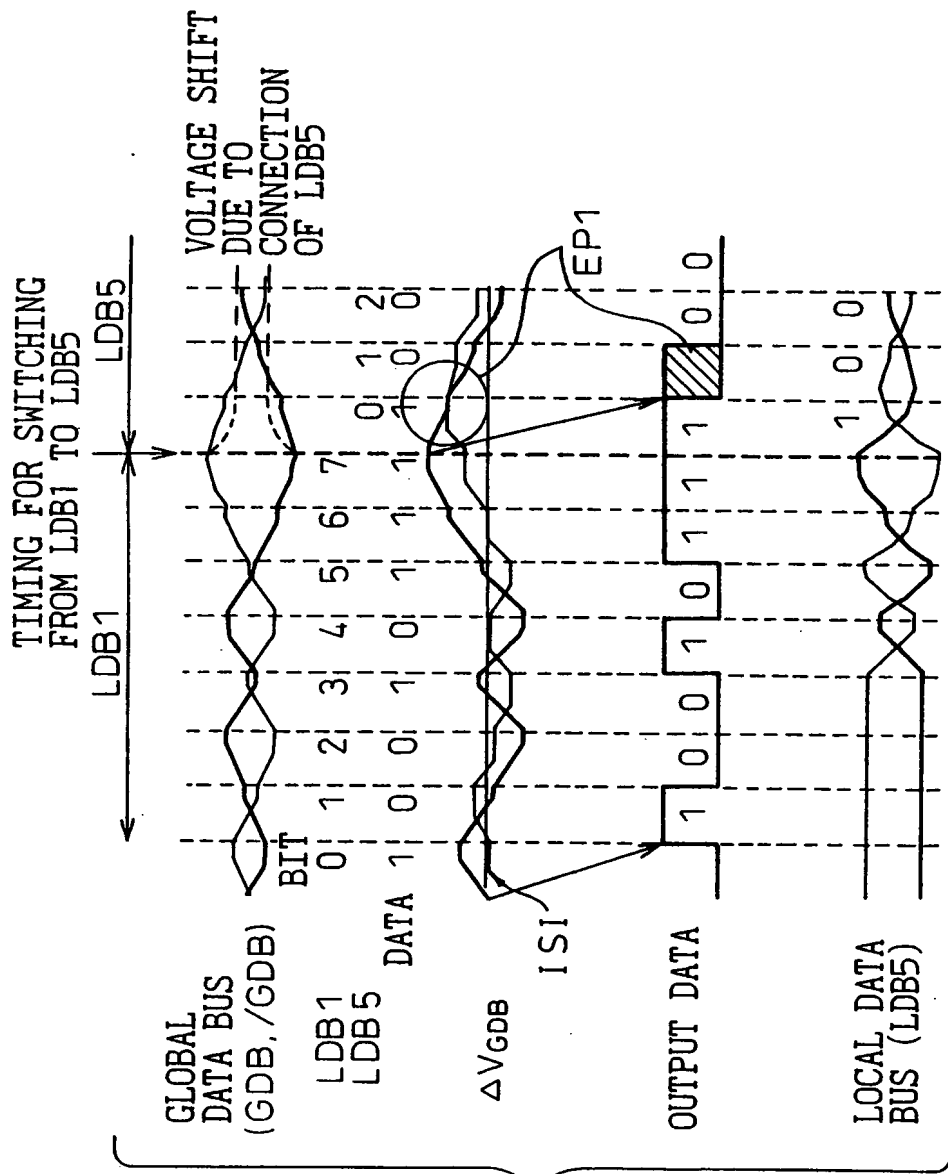


Fig.16
(RELATED ART)



Title: SIGNAL TRANSMISSION SYSTEM USING PRO METHOD, RECEIVER CIRCUIT FOR USE IN THE SIGNAL TRANSMISSION SYSTEM, AND SEMICONDUCTOR MEMORY DEVICE TO WHICH THE SIGNAL TRANSMISSION SYSTEM IS APPLIED
 Inventor: KUMAGAI et al.
 Application No. 09/714,285
 Docket No. 100021-00033

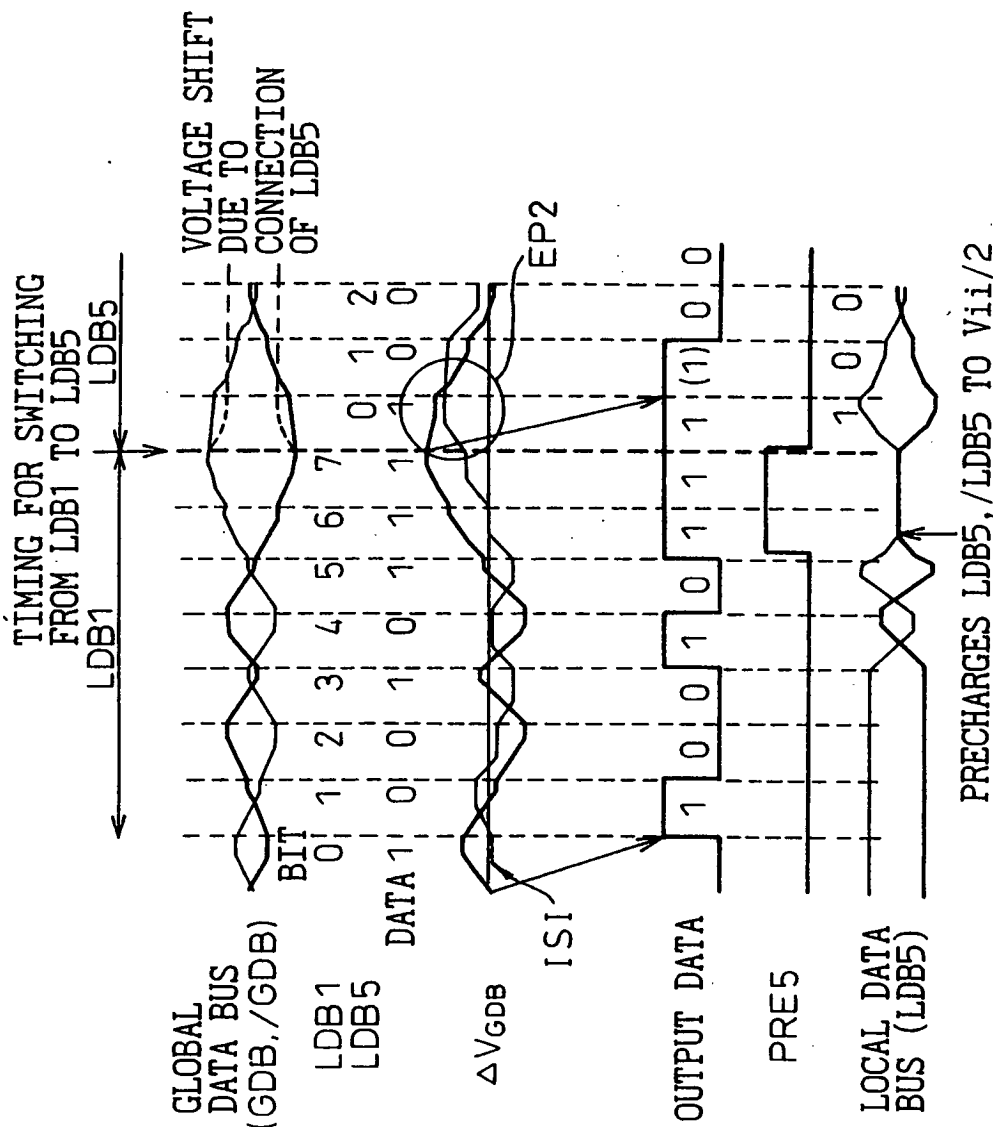


Fig.17
 (RELATED ART)